

The diagram illustrates a printing system 100. A dashed line separates the system components from an external host system 106. The system includes a controller 110, an ink supply device 112, a power supply 114, and a printhead assembly 116. The ink supply device 112 contains a memory 118. The printhead assembly 116 contains a memory 122, a processing driver head 126, a data processor 124, and a driver head 120. The host system 106 is connected to the controller 110. The controller 110 is connected to the ink supply device 112, the power supply 114, and the printhead assembly 116. The ink supply device 112 is connected to the printhead assembly 116. The power supply 114 is connected to the controller 110 and the printhead assembly 116. The printhead assembly 116 is connected to the controller 110 and the power supply 114.

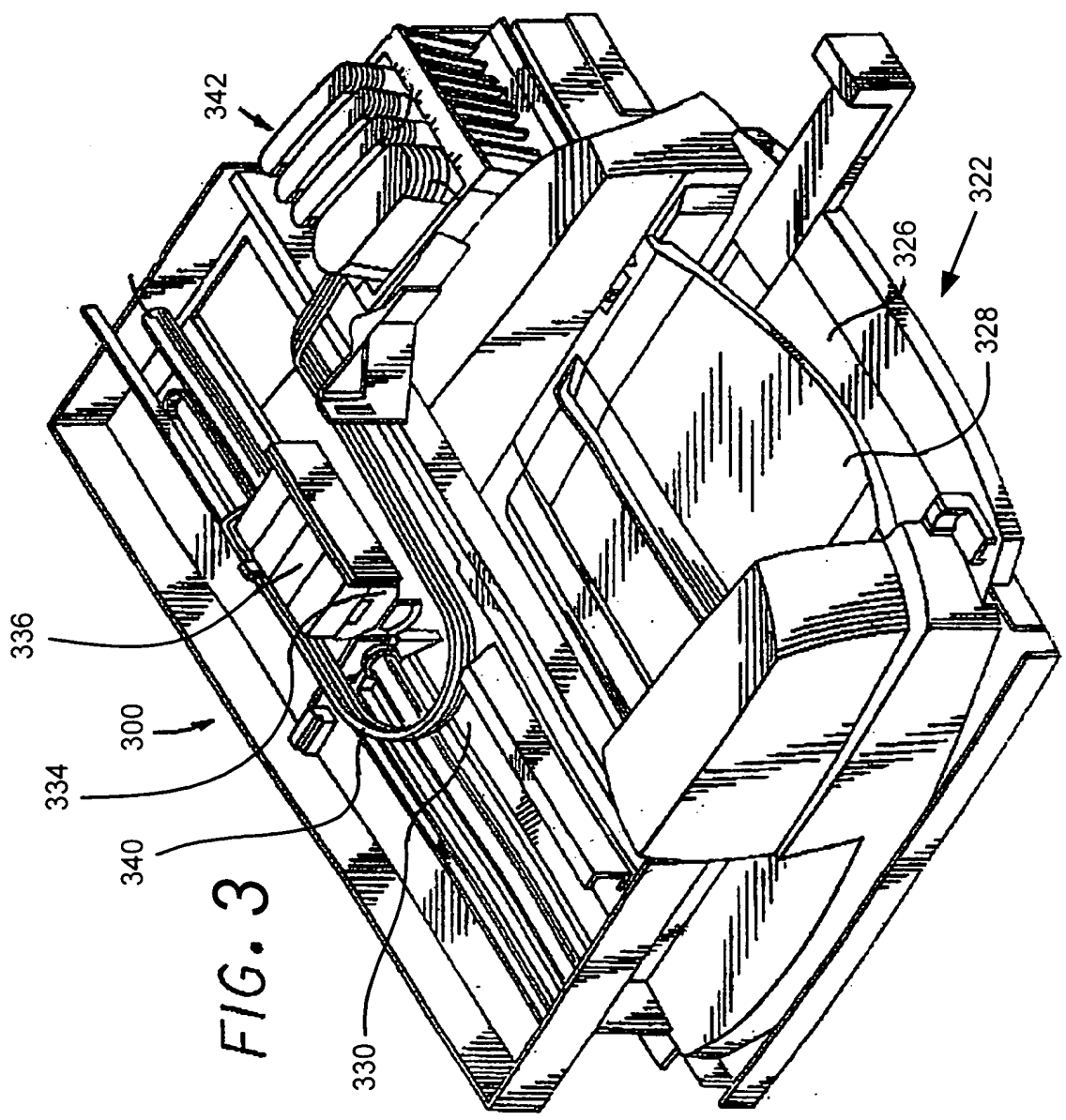
FIG. 1

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graph TD
    210[CONFIRM THAT PRINthead HAS A SYSTEMATIC INK DROP PLACEMENT ERROR] --> 212[DEVELOP SCHEME TO CORRECT THE ERROR]
    212 --> 214[OPTIONALLY ENCODE AMOUNT OF ERROR ON A MEMORY DEVICE]
    214 --> 216[ASSOCIATE/INCORPORATE CORRECTION SCHEME WITH PRINthead (OPTIONALLY WITH MEMORY DEVICE)]
    216 --> 218[APPLY CORRECTION SCHEME TO PRINthead DURING PRINTER OPERATIONS]
    218 --> 220[EJECT INK DROPS FROM PRINthead ONTO MEDIA WITH CORRECTED ALIGNMENT]

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FIG. 2



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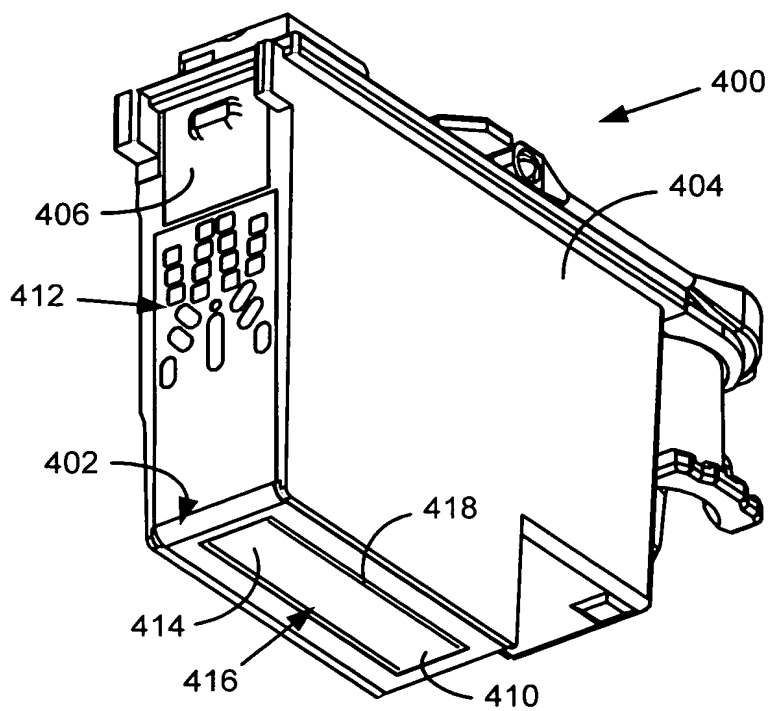


FIG. 4

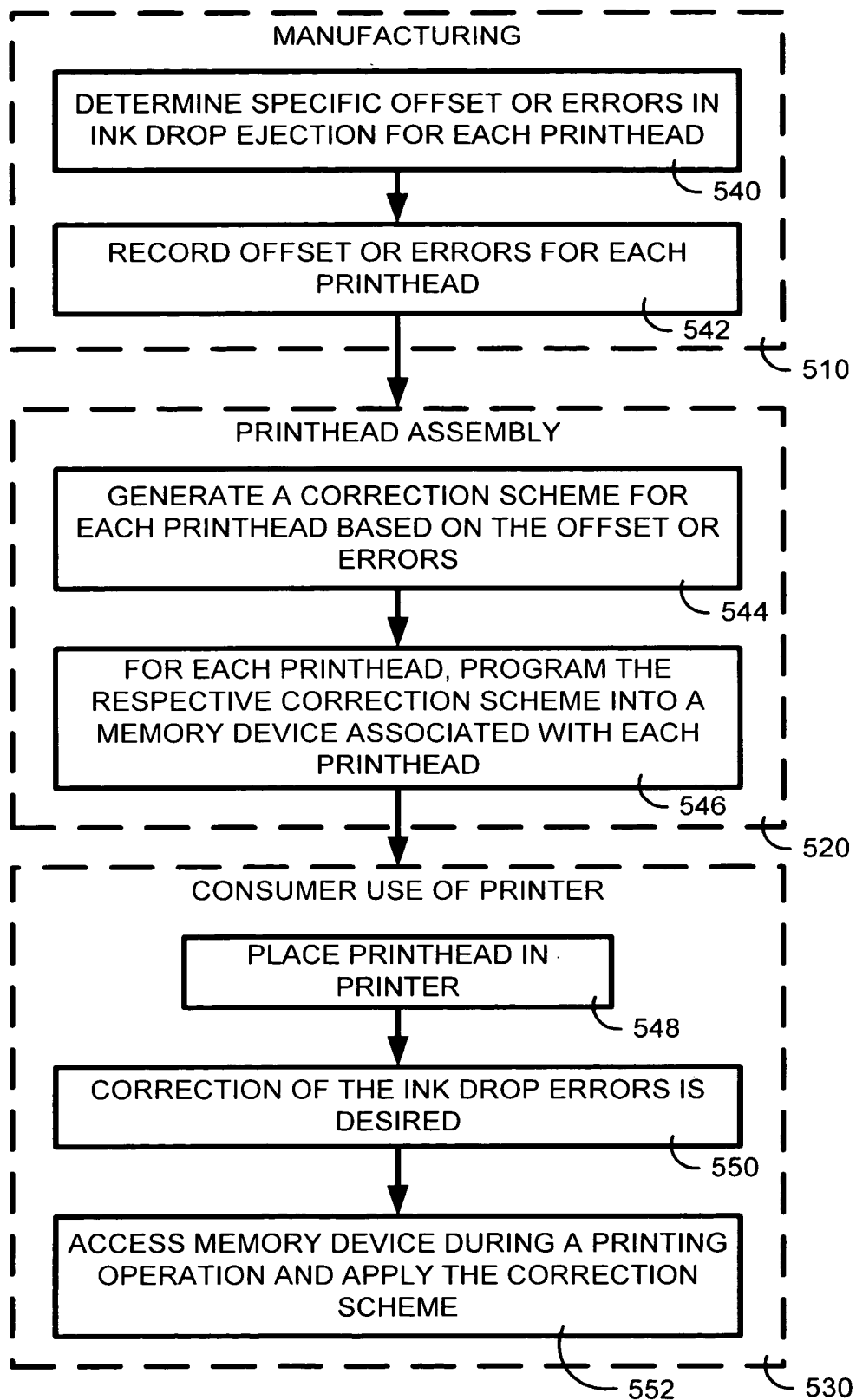
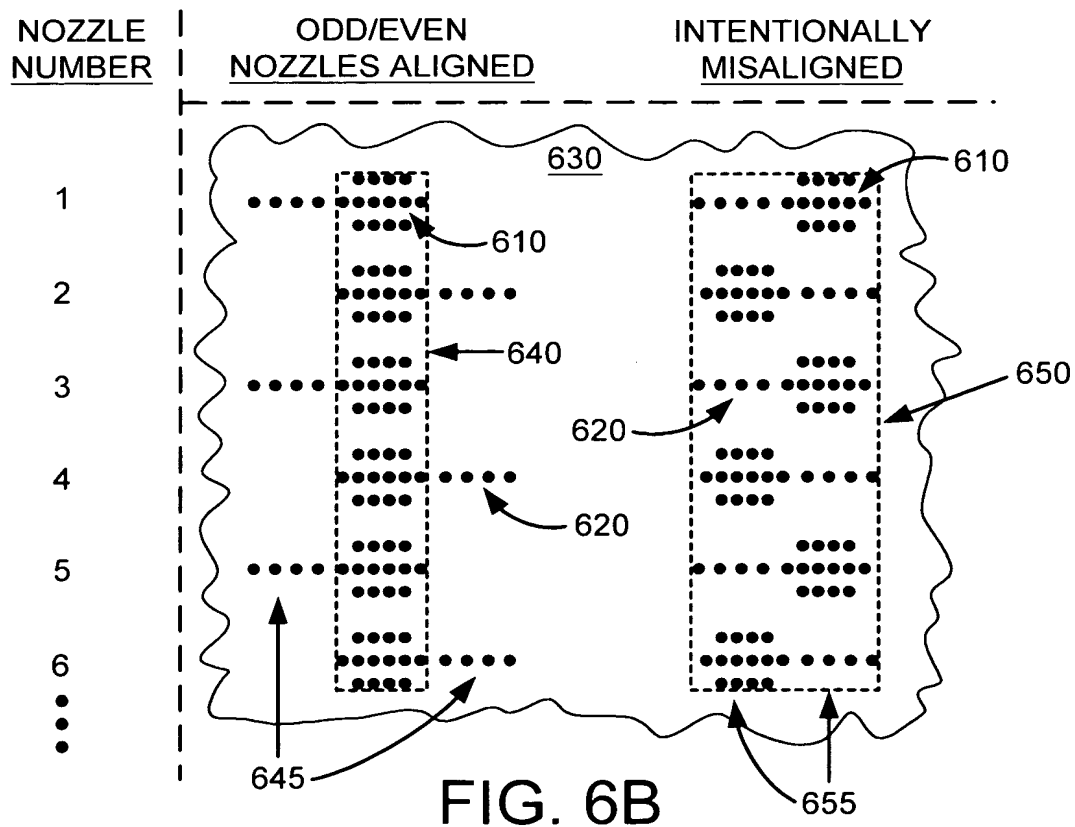
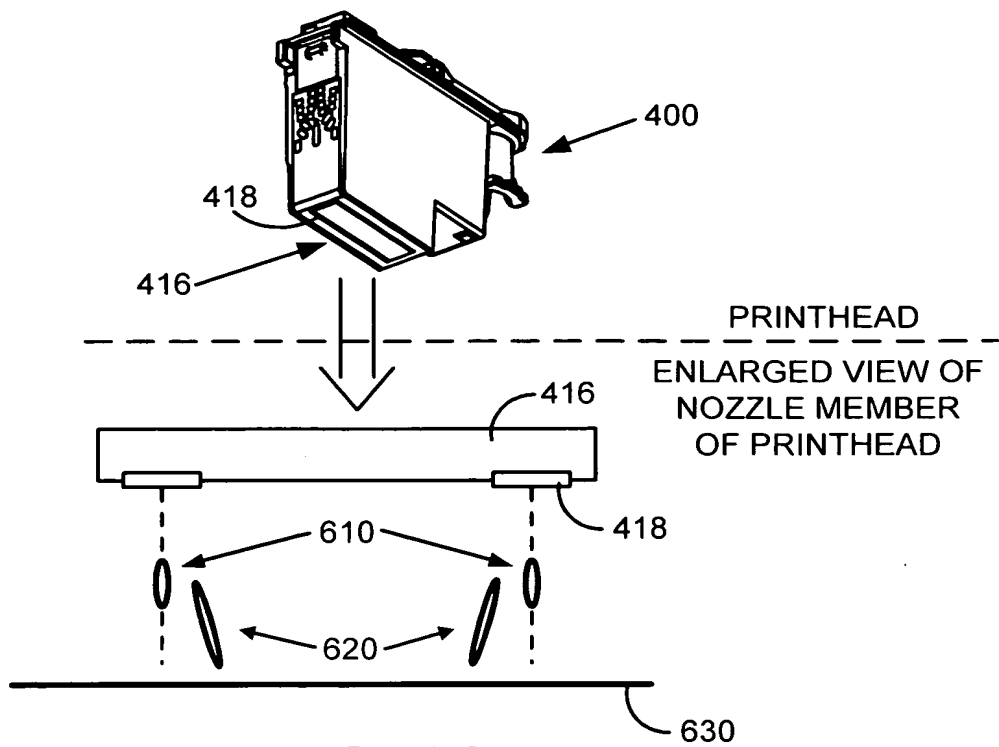


FIG. 5



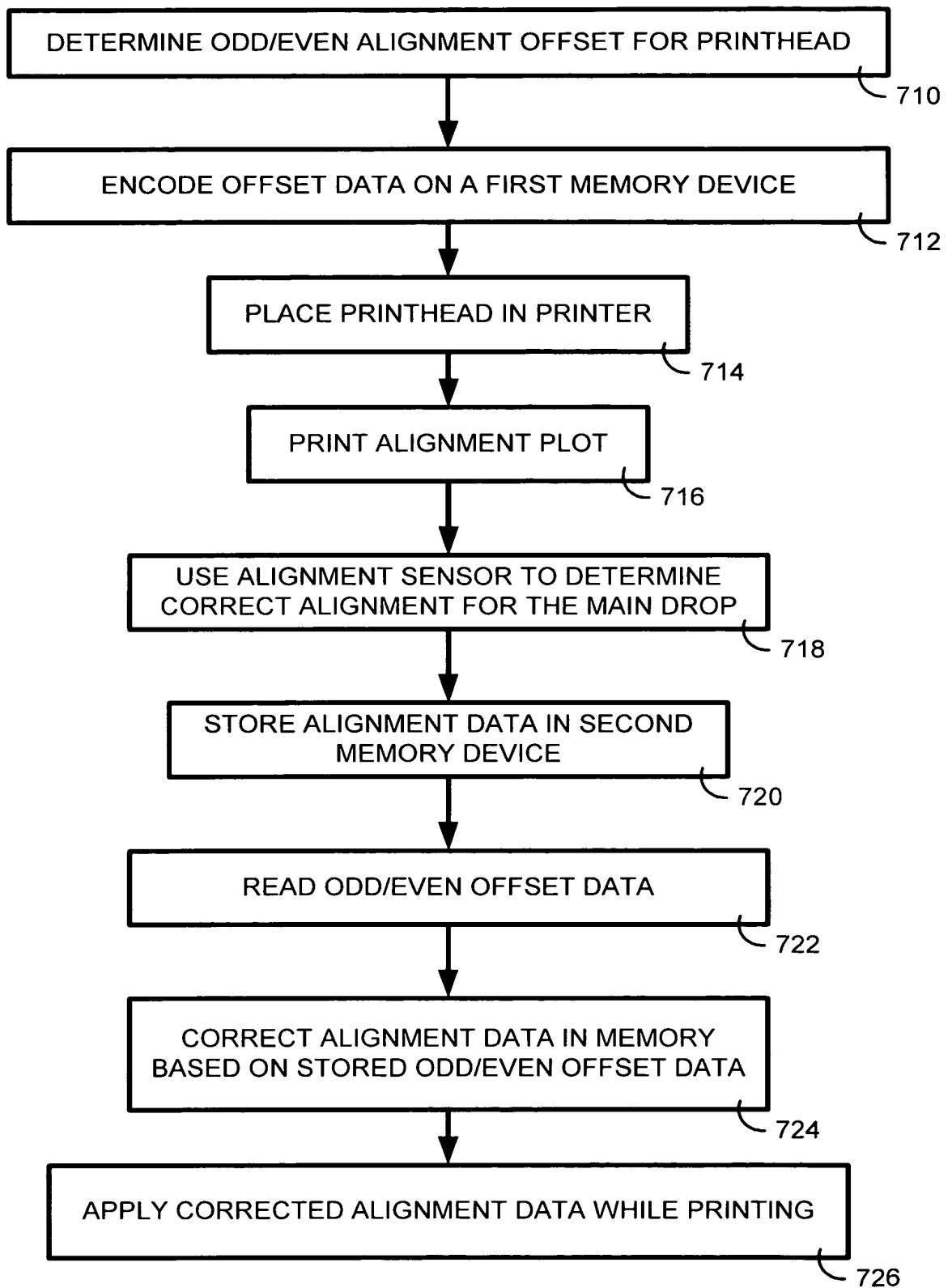


FIG. 7

NOZZLE NUMBER	ONE PASS SCANNING L/R ==>	ONE PASS SCANNING <== R/L	TWO PASS BIDI SCANNING ALIGNED	TWO PASS BIDI SCANNING WITH INTERNATIONAL BIDI ALIGNMENT OFFSET
1				
2				
3				
4				
5				
6				

FIG. 8

CONVENTIONAL MASK	COMPENSATED MASK
[(0) ()] [(1) ()]	[() (0)] [() (1)]
[(0) ()] [(1) ()]	[(0) ()] [(1) ()]
[(1) ()] [(0) ()]	[() (1)] [() (0)]
[(1) ()] [(0) ()]	[(1) ()] [(0) ()]

FIG. 9